



# PUNYASHLOK AHILYADEVI HOLKAR SOLAPUR UNIVERSITY, SOLAPUR

## EXAM OF 2020 SEMESTER PATTERN EXAMINATION

### FINAL PROGRAM OF M.SC. (ELECTRONICS) SEMESTER – I TO IV MARCH 2020 EXAMINATION TO BE HELD IN OCT-2020

1. The Candidates at the above examination are requested to be present **FIFTEEN MINUTES** before the time starting of the first paper and **TEN MINUTES** before the time starting of each subsequent paper. Candidates are forbidden to take any book or paper into the Examination Hall.
2. The Examination Programme is subject to change. If there is any change in the Examination Programme the University is not responsible for the loss or inconvenience caused to the candidates.
3. The external candidates are requested to see their examination seat number as well as place of the examination on the notice board of the University/College at least two days before the date of examination.
4. Cellular Phone, Pager, Scientific Calculator, Digital Diary or any means which can be used for unfair practice, are not allowed in the Examination Hall.
5. **Important Note:** Code Numbers given in the Bracket are Computer Code of respective Subjects, Students should mention these Code Numbers on Answer Books with Name of the Subject. Also write these Code Numbers on JSR (Junior Supervisor Report) and related documents.
6. The Time for the exam is one and haf hour (1 ½) only.
7. The written examination will be conducted online/offline mode which will be held at home/deseignated places.
8. All the student are here by informed that the time slot given in the time table shows 5 hrs for each day. But you will get 1 ½ hrs to solve the paper. If any student has two paper in one day that time he will be solving two papers within 5 hours.

<b>Electronics Sem – I (w.e.f. June 2016)</b> <b>(CBCS Pattern-2016)</b>			
Day & Date	Paper No.	Sr. No.	Subject <b>TIME: 11:00 a.m. to 4:00 p.m.</b> <b>(Only 1 ½ hrs out of this slot)</b>
<b>Monday</b> <b>05/10/2020</b>	HCT1.1	216	Numerical Methods (21720101)
<b>Tuesday</b> <b>06/10/2020</b>	HCT1.2	217	Instrumentation Design (21720102)
<b>Wednesday</b> <b>07/10/2020</b>	HCT1.3	218	Power Electronics (21720103)
<b>Thursday</b> <b>08/10/2020</b>	SCT1.1	219	<b>A) Advanced Microcontrollers (21720104)</b>
	SCT1.2	220	<b>OR</b> <b>B) Virtual Instrumentation (21720105)</b>

<b>Electronics Sem – II (w.e.f. Nov 2016)</b> (CBCS Pattern-2016)			
Day & Date	Paper No.	Sr. No.	Subject <b>TIME: 11:00 a.m. to 4:00 p.m.</b> <b>(Only 1 ½ hrs out of this slot)</b>
<b>Monday</b> <b>05/10/2020</b>	HCT2.1	221	Control Theory (21720201)
<b>Tuesday</b> <b>06/10/2020</b>	HCT2.2	222	Real Time Operating System (21720202)
<b>Wednesday</b> <b>07/10/2020</b>	SCT2.1 SCT2.2	223 224	<b>A) Opto Electronics (21720203)</b> <b>OR</b> <b>B) Agro Instrumentation (21720204)</b>
<b>Saturday</b> <b>17/10/2020</b>	OET	--	<b>[Choose from list]</b>

<b>Electronics Sem – III (w.e.f. June 2017)</b> (CBCS Pattern-2016)			
Day & Date	Paper No.	Sr. No.	Subject <b>TIME: 11:00 a.m. to 4:00 p.m.</b> <b>(Only 1 ½ hrs out of this slot)</b>
<b>Monday</b> <b>05/10/2020</b>	HCT3.1	225	Digital Signal Processing (21720301)
<b>Tuesday</b> <b>06/10/2020</b>	HCT3.2	226	Advanced Digital Design with VHDL (21720302)
<b>Wednesday</b> <b>07/10/2020</b>	SCT3.1 SCT3.2	227 228 229 230	<b>A) ARM Microcontroller and system design (21720307)</b> <b>OR</b> <b>B) CMOS Design Technologies (21720313)</b> <b>OR</b> <b>C) Computer Aided VLSI Design (21720308)</b> <b>OR</b> <b>D) Optical Fiber Communication (21720311)</b>
<b>Sunday</b> <b>18/10/2020</b>	OET	--	<b>[Choose from list]</b>

<b>Electronics Sem – IV (w.e.f. Nov 2017)</b> <b>(CBCS Pattern-2016)</b>			
Day & Date	Paper No.	Sr. No.	Subject <b>TIME: 11:00 a.m. to 4:00 p.m.</b> <b>(Only 1 ½ hrs out of this slot)</b>
<b>Monday</b> <b>12/10/2020</b>	HCT4.1	231	Microwave Devices, Antennas and Measurements (217204-01)
<b>Tuesday</b> <b>13/10/2020</b>	HCT4.2	232	Networking and data communications (217204-02)
<b>Wednesday</b> <b>14/10/2020</b>	HCT4.3	233	Nanoelectronics (217204-03)
<b>Thursday</b> <b>15/10/2020</b>	SCT4.1	234	<b>A] Mechatronics and Industrial Automation (217204-06)</b>
		235	<b>OR</b> <b>B] Satellite Communication (217204-07)</b>
	SCT4.2	236	<b>OR</b> <b>C] Wireless Sensor Network (217204-08)</b>
		237	<b>OR</b> <b>D] Mixed Signal Based SoC Design (217204-09)</b>
		238	<b>OR</b> <b>E] Smart Fusion Technology Based System Design(217204-10)</b>

Please visit - <http://su.digitaluniversity.ac>

Ref No.: PAHSUS/EXAM/TIME-TABLE/2020/778

Date : 19/09/2020

Sd/-

**I/c Director**

Board of Examinations and Evaluation