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### M.Sc. (Part – I) (Semester – I) Examination, 2015

		•	ommunication Sunication Sunication System		
Day an	d Date : Monday	, 20-4-2015		Total Marks:	70
Time:	11.00 a.m. to 2.0	00 p.m.			
	<i>2) 3)</i>	<b>All</b> questions carr	questions from Q.	<b>3</b> to Q. <b>7</b> .	0
1. A) S	elect correct alt	ernatives.			8
1)	A 400-watt car in the modulate		o a depth of 80%. Ca	alculate the total power	
	a) 656 watt	b) 528 watt	c) 324 watt	d) 264 watt	
2)	An unmodulate then m is	ed carrier is 10 W	measured at 12 W	when modulated, and	
	a) 63%	b) 82%	c) 25%	d) 50%	
3)	The modulatio	n index of FM syst	em is		
	a) > 1	b) < 1	c) = 1	d) none of the above	ļ
4)		by deviation is $\pm 5$	60 kHz and message	e signal frequency is	
	a) 5 degree	b) 6 radian	c) 6 degree	d) 5 radian	
5)	Indicate which	of the following sy	stem is analog.		
	a) PCM		b) Differential P	PCM	
	c) PWM		d) Delta		

6

		6)	Time Division multiplexing is  a) Interleaving pulses belonging to different transmission b) Can be used with PCM only c) Combines five groups into adjacent frequency slot d) Stacks 24 channels in adjacent frequency slot	
		7)	State, which characteristics of a PWM change with modulation a) Frequency b) Amplitude c) Phase d) Duty cycle	
		8)	With increase in the sampling frequency  a) The adjacent spectrums will overlap  b) A guard band is created between the adjacent spectrums  c) The adjacent spectrums will touch each other  d) None of the above	
	B)	Sta	ate <b>true</b> of <b>false</b> .	6
		1)	PPM is obtained from PAM.	
		2)	Selectivity of a receiver is to spread one signal from other on closely adjacent frequency.	
		3)	In RZ, encoding process is based on the data bits.	
		4)	PLL is negative feedback system.	
		5)	FM broadcast stations are assigned adjacent channels to operate in the same region.	
		6)	In A.M. if modulation index is <1, the baseband signal is recovered with distortion.	
2.	A <sup>-</sup>	tter	mpt the following.	
	1)	E	xplain the need of pre-emphasis and de-emphasis in FM.	5
	2)	) W	hat are the advantages of single side band transmission?	4
	3)	S	tate and explain the sampling theorem.	5
3.	a)		or A.M. transmitter, using the crystal controlled price oscillator, temperature ven and buffer amplifier explain the exciter section.	8

b) Explain the design of class C amplifier.



4.	<ul> <li>a) Explain in detail VCO master oscillator for F.M. transmitter for generation of carrier.</li> </ul>	10
	b) Explain PLL as F.M. detector.	4
5.	a) Explain the generation direct sequence spread spectrum.	8
	b) Explain PN sequence generation.	6
6.	a) Explain differential PSK system in detail.	8
	b) Explain the frequency doubler concept in FM.	6
7.	a) Explain the generation of PWM and PPM with suitable circuit diagram.	8
	<ul> <li>b) 10111000 express the binary signal in following data format.</li> <li>i) Unipolar RZ</li> <li>ii) Bipolar RZ</li> <li>iii) Unipolar NRZ</li> <li>iv) Bipolar NRZ.</li> </ul>	6

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P	ape		ELE	ECTRONICS ( : Introduction	Comm	uı	nic	atio	n S	cience)	/ (New)	
Day and D	ate	: We	dne	sday, 22-4-2015							Max. Ma	arks : 70
Time : 11.0	00 a	.m. 1	to 2.	00 p.m.								
	Inst	ruci	tion	<b>s</b> : 1) Q. <b>1</b> and Q	. <b>2</b> are	cc	mp	ouls	ory.			
				2) Answer <b>an</b>	y three	qı	ues	tions	s fro	m Q. <b>3</b> to C	Q. <b>7</b> .	
				3) <b>All</b> questio	ns carry	/ <b>E</b>	qu	<b>al</b> m	arks			
1. a) Se	elec	t coı	rect	option :								8
1)	A	MAT	LAB	s variable name r	nay hav	e	max	ximu	ım o	f	_charact	ers.
	a)	10		b) 220	c	;)	68			d) 131		
2)	All	eler	ment	ts of column vect	tor in MA	٩T	LA	Bar	e se <sub>l</sub>	parated by		_•
	a)	Bla	nk sį	oace	b	)	Co	nma	as			
	c)	Bar	nk sp	ace or commas	C	1)	Ser	nico	lon			
3)	) If a	a (2,	3) =	4, then								
	a)	0	0	0	b	))	0	0	0			
		0	0	4			0	0	4			
		2	3	4								
							_	•	•	•		
	c)	1			C	1)			0	0		
		0					0			0		
		0	4				0	0	1	0		

b)



4)	is a sequin LabVIEW.	ence of display	able (	or non-display	able ASCII characters	
	a) numeric	b) string	c)	cluster	d) script node	
5)	Terminal blocks	of DAQ device	have	te	erminals.	
	a) 100		b)	68		
	c) 50		d)	any one of the	e above	
6)	Logical operator	'~' is		_•		
	a) logical OR		b)	logical exclus	sive-OR	
	c) logical compl	iment	d)	logical AND		
7)	Highlight option i	s present on _		_•		
	a) Front panel to	oolbar	b)	Front panel fu	unction	
	c) Block diagrar	n toolbar	d)	Block diagrar	n control	
8)	I stands for	in while lo	oop a	nd for loop of	LabVIEW.	
	a) iteration number	ber	b)	indexing num	ber	
	c) integration nu	ımber	d)	none of the a	bove	
Sta	ate <b>True</b> or <b>False</b>	<b>)</b> :				6
1)	In the MATLAB	% is used for ba	acksp	oace.		
2)	Virtual instrumer	ntation has synd	chron	nizing platform		
3)	) If-else control structure has two group of statement true and false.					
4)	MATLAB workspace is a collection of all the variables that have been generated so far in the current MATLAB session and shows their data type and size.					
5)	A group of homo cluster.	ogeneous elem	ents (	of a specific d	ata types is known as	

6) LabVIEW installs MAX which establishes all devices and channel

configuration parameter?



		<b>J</b>	
2.	Attempt the following:		
	1) Explain Matrix subscript of MATLAB.		5
	2) Explain Transducers for DAQ.		5
	3) Explain in brief Numeric constants of MATLAB.		4
3.	a) Draw and explain graphical system design model.		10
	b) List out four types of LabVIEW clusters and explain it.		4
4.	a) Explain briefly MATLAB environment with its help features.		10
	b) Define switch statement of MATLAB using general syntax.		4
5.	a) Define sub VI in LabVIEW. How to create an icon in LabVIEW	<b>'</b> ?	10
	b) Write a note on DAQ-card.		4
6.	a) Explain briefly the types of MATLAB functions.		10
	b) Write a program to obtain the sum of all even numbers from 0	to 20, using	
	'for' statement.		4
7.	a) Draw G-code for calculator and half adder in LabVIEW.		10
	b) Explain MATLAB 'while' loop.		4



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c) both (a) and (b)

#### M.Sc. I (Semester – I) Examination, 2015 ELECTRONICS (Communication Science) Paper – II (Old) Instrumentation

Paper - II (Old) Instrumentation Total Marks: 70 Day and Date: Friday, 17-4-2015 Time: 11.00 a.m. to 2.00 p.m. Instructions: 1) Q. (1) and (2) are compulsory. 2) Answer any three questions from Q. 3 to Q. 7. 3) All questions carry equal marks. 14 1. Objective questions. a) Choose correct alternatives. 8 1) A thermistor operates on the principle, a) variable inductance b) variable capacitance c) variable resistance d) all of the above 2) Piezo-electric crystal is used to measure a) sound b) light c) pressure d) none of the above 3) In microphone, which of the following varies with the input audio signal, a) resistance b) capacitance d) none of the above c) inductance 4) UPS converts a) ac into dc b) dc into ac

d) none of the above

2.



	5)	Spectrum analyser is used to study			
		a) static characteristics	b)	dynamic characteristics	
		c) energy distribution of signal	d)	none of the above	
	6)	A pH of less than 7, indicates the so	olut	ion is,	
		a) acidic	b)	alkaline	
		c) neutral	d)	all of the above	
	7)	A multiplexer has			
		a) one input, many outputs	b)	many inputs, one output	
		c) one input, one output	d)	many inputs, many outputs	
	8)	A data acquisition systems must ha	ve		
		a) ADC and DAC	b)	Multiplexer and demultiplexer	
		c) Sample and hold circuit	d)	All of the above	
b)	Fil	l in the blanks.			6
	1)	Thermocouples are used to measure	·	·	
	2)	In solar cells, light is converted into			
	3)	In S/H circuits, are u	ise	d as switch.	
	4)	amplifiers are used t	ор	revent high voltage signals to a	
		system.			
	5)	UPS requiresfor it	ts o	peration.	
	6)	Human hair can be used to measure			
Att	tem	pt <b>any three</b> .			14
a)	WI	hat is Hall effect ? Explain.			
b)	Dis	scuss piezoelectric sensors.			
c)	Ex	plain the working of an inverse trans	duc	er.	
d)	Wı	rite a note on proximity detector.			



3.	a)	Explain the construction and working of various types of temperature transducers.	10
	b)	Write the advantages and disadvantages of each of the above transducers.	4
4.	a)	With neat diagram, describe the functioning of a temperature balance system.	10
	b)	Explain the operation of a RMS converter.	4
5.	a)	With necessary diagram, discuss in detail, the working of a SMPS.	10
	b)	Write a note on frequency to voltage converter.	4
6.	a)	Draw the diagram of a lock-in amplifier and explain its operation.	10
	b)	Write a note on power measurement.	4
7.	a)	Discuss the working of a standard interface system in detail.	10
	b)	Write a note on signal conditioning.	4



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## M.Sc. (Part - I) (Semester - II) Examination, 2015

-	ELECTRONICS (Communication Science) (New) Paper - V: Modern Antenna Design  ay and Date: Thursday, 16-4-2015  Total Marks: 70  me: 11.00 a.m. to 2.00 p.m.  Instructions: 1) Q. (1) and (2) are compulsory. 2) Attempt any three questions from Q. 3 to Q. 7. 3) All questions carry equal marks.  A) 1) Power level of side lobes of an antenna is					
	Ins	2) A	ttempt any three	questions from Q.	<b>3</b> to Q. <b>7</b> .	<b>8</b>
1. A)	1)	Power level of s	ide lobes of an ant	tenna is		8
,	,					
	2)	For deep-dish re	eflectors, f/D ratio	is	_	
		a) large	b) 1	c) small	d) greater than 1	
	3)					
		a) wire	b) aperture	c) microstrip	d) lens	
	4)	radius is			-	
		a) Field pattern		b) Power pattern		
	5)					
		•	•	-		
		•				
		•	•			
	6)	•	•	owei		
	O)			h) Hịnh nain nai	rrow hand antenna	
	7)	•		•		
	٠,			otoro aro approxima	tory oqual in both	
			• •	c) 90°	d) 45°	
	8)	Radiation patter	,	,	•	
	,	•	<b>G</b>		al	
		c) Bidirectional		d) None of the abo	ove	

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B) Write a note on Cassegrain feed.

antenna.

	3 4 5	<ul> <li>Half wave dipole is very useful as a multi-band HF receiving antenna.</li> <li>The radiation pattern of paraboloidal reflector antenna with a horn antenna and the radiation pattern of paraboloidal reflector antenna with feed are same.</li> <li>The region of Radiative near-field region is in between the reactive region and the far field region.</li> <li>Half power beam width is half of the first null beam width.</li> <li>The end fire array is the broadside array.</li> <li>Intrinsic impedance of free space is 65 Ω.</li> </ul>	6
2.	Atte	mpt the following :	
	1) E	xplain briefly radian and steradian.	5
	2) L	ist the application of an antenna.	4
	3) V	/rite a note on radiation intensity.	5
3.	A) D	erive an expression for retarded potential of an antenna.	10
	B) E	xplain the field regions of an antenna.	4
4.	A) D	escribe non-metallic dielectric lens antenna with its suitable diagram.	10
	B) D	efine the term of directivity of an antenna.	4
5.	•	Obtain an expression for the pattern in the gain over a $\lambda/2$ dipole antenna in ee space with an arbitrary power input.	10
	B) E	xplain the various types of radiation pattern.	4
6.	A) D	raw and formulate the radiation mechanism of a single wire.	10

7. A) Explain in detail with suitable diagram the various types of corner reflector

B) Explain briefly the lobe and the various types of lobes.

4

10

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## M.Sc. (Part – I) (Semester – II) Examination, 2015 ELECTRONICS (Communication Science) (New) (Paper – VI) Microwave Engineering

Microwave I		'/
Day and Date: Saturday, 18-4-2015 Time: 11.00 a.m. to 2.00 p.m.	Total M	larks : 70
Instructions: 1) Q. (1) and (2) are c 2) Answer any three 3) All questions carry	questions from Q. 3 to Q. 7.	
1. Objective Questions:		14
a) Select correct alternatives :		6
<ol> <li>When transmission line is provide coefficient is</li> </ol>	ed with shorting plate the load reflec	ction
a) 0	b) 1	
c) -1	d) None of the above	
2) X-band microwave frequency lies	s in the range of	
a) 8 – 12 GHz	b) 2 – 4 GHz	
c) 4-8 GHz	d) 12 – 16 GHz	
3) TE <sub>10</sub> is dominant mode of propag	ation in	
a) Circular wave guide		
b) Rectangular wave guide		
c) Coaxial line		
d) Square wave guide		
4) Transverse electric wave has		
a) $E_z = H_z = 0$	b) $H_z = 0$	
c) $E_{7} = 1$	d) $E_7 = 0$	

5) Reflex Klystron is \_\_\_\_\_



		a) Microwave amplifier	
		b) Microwave oscillator	
		c) Microwave passive device	
		d) Microwave phase shifter	
		6) In the case of lossless transmission line one of the following conditions are valid,	
		a) $R = 0$ b) $G = 0$	
		c) $R = G = 0$ d) $L = C = 0$	
	b)	State <b>true</b> or <b>false</b> :	8
		<ol> <li>There is no phase difference between E and H fields of the EM wave propagating in a perfect insulator.</li> </ol>	
		2) Gunn diode can be used as a microwave oscillator.	
		3) At 5 GHz frequency transmission of E.M. wave using tapered load can be done.	
		4) Tangential component of electric and magnetic field are continuous across the boundary.	
		5) SWR is always equal to or greater than unity.	
		6) In a coaxial line if $R_L = Z_0$ , then load reflection coefficient $T_L$ is infinite.	
		7) TE <sub>00</sub> Mode exists in the rectangular waveguide.	
		8) A circularly polarized wave is resultant difference of two equal amplitudes fields vectors in phase quadrant.	
2.	a)	Explain electric fields in conducting material.	5
	b)	Explain Maxwell's equation in integral form.	5
	c)	Explain the concept of lossy dielectric.	4
3.	a)	Explain in detail the working principle and concept of velocity modulation with the help of suitable diagram.	8
	b)	Explain dielectric phase shifter in the rectangular waveguide.	6



4. a) Explain the theory of rectangular waveguide transmission for TE mode. 10 b) A transmission line has characteristic impedance of 50 +  $i0.01'\Omega$  and terminated in a load impudence of 73 – j42.5' $\Omega$ . Calculate : a) The reflection coefficient b) The standing wave ratio. 4 5. a) A coaxial line has the following characteristics at 1000 MHz: R' = 4 ohms/m. L' = 450 nH/m,  $G' = 7 \times 10^{-4} \text{mho/m}$ , C' = 50 pF/m. 10 i) Calculate  $Z_0$ ,  $\alpha$ ,  $\beta$  and  $\lambda$  at 1000 MHz. ii) With  $V_0^+=10|0$  V and  $V_0^-=0$ , Calculate V, I and P at z=4 m. b) Short note on low loss lines. 4 6. a) Explain in brief rotary phase shifters with suitable vector diagrams. 8 b) Explain the Gunn effect principle in detail. 6 7. a) Sketch and explain Type-N connector. 8 b) Sketch and explain resistive card waveguide attenuator. 6



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#### M.Sc. (Part – I) (Semester – II) Examination, 2015 ELECTRONICS (Communication Science) (New) Paper – VII: Advanced Microcontrollers

	Paper	r – VII : Advan	ced Microcontrol	lers	
-	Date : Tuesday, 2 00 a.m. to 2.00 լ			Total Marks :	70
Insti	2) A	D. <b>1</b> and <b>2</b> are <b>con</b> Inswer <b>any three</b> All questions carry	questions from Q. 3	to Q. <b>7</b> .	
1. Objec	ctive questions :				14
a) Se	elect correct alte	rnatives :			8
1)	ARM is a	arch	itecture.		
	a) RISK	b) CISK	c) RISC	d) CISC	
2)	ARM thumb mo	ode has a	bit instruct	tion set.	
	a) 8	b) 16	c) 32	d) 64	
3)	_		adthe internal SRAM.	dditional clock cycle	
	a) 0	b) 1	c) 2	d) 3	
4)		is a conditiona	al branch instruction o	of AVR.	
	a) BRNE	b) BREQ	c) SBRC	d) All of these	
5)		instruction fo	rces program execu ster.	ution to continue at	
	a) RJMP	b) RCALL	c) IJMP	d) All of these	
6)		ing I/O registers o	f AVR as SRAM,		
	a) \$10	b) \$16	c) \$20	d) \$64	

2.

3.

4.

5.

6.

7.



7) R 15 of ARM is a		
a) Stack pointer	b) Link register	
c) Program counter	d) CPSR	
8) The output of "ADD r3, r2, r1, LSL	# 3" ARM instruction is	
a) $(r3 = r2 + 8 \times r1)$	b) $(r1 = r3 + 8 \times r2)$	
c) $(r3 = r2 + 3 \times r1)$	d) $(r1 = r3 + 3 \times r2)$	
State <b>True</b> or <b>False</b> :		6
1) AVR's "CPI Rd, K" instruction co	pies immediate number K into Rd.	
2) AVR RESET input is active high		
3) AVR has LPM instruction for coo	le memory addressing.	
,	_	
<ol><li>ARM PC runs ahead of the current the next but one instruction.</li></ol>	ent instruction and holds the address of	
rite short notes :		
Write a short note on AVR GPR file.		5
Write a short note on Endianness.		5
Explain execution of ARM instructio	n "ADD r0, r0, r1" in pipeline.	4
With a neat diagram explain AVR RI	SC architecture.	8
Give list of features of AVR.		6
Explain following addressing modes Addressing, I/O Direct Addressing.	of AVR : Direct Single Register	6
Explain interfacing external SRAM to	AVR.	8
Write a program to generate square	wave on PORTA of AVR.	8
Explain operating modes of ARM.		6
List ARM processor families and give	e their important features .	8
Explain exception processing in ARI	M.	6
Give a brief overview of ARM instru	ction set (any eight).	8
Explain arithmetic, logical and shift i	nstructions in AVR.	6
	a) Stack pointer c) Program counter 8) The output of "ADD r3, r2, r1, LSL a) (r3 = r2 + 8 × r1) c) (r3 = r2 + 3 × r1)  State <b>True</b> or <b>False</b> : 1) AVR's "CPI Rd, K" instruction co 2) AVR RESET input is active high 3) AVR has LPM instruction for co 4) ARM's system mode uses the us 5) ARM memory system supports to 6) ARM PC runs ahead of the curre the next but one instruction.  rite short notes:  Write a short note on AVR GPR file.  Write a short note on Endianness.  Explain execution of ARM instruction With a neat diagram explain AVR RI Give list of features of AVR.  Explain following addressing modes Addressing, I/O Direct Addressing.  Explain interfacing external SRAM to Write a program to generate square Explain operating modes of ARM.  List ARM processor families and give Explain exception processing in ARI Give a brief overview of ARM instruction.	a) Stack pointer c) Program counter d) CPSR  8) The output of "ADD r3, r2, r1, LSL # 3" ARM instruction is

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M.Sc. (Part – I) (Semester – II) Examination, 2015 ELECTRONICS (Communication Science) (New) Paper – VIII: Digital Design and VHDL Programming

Day and Date: Thursday, 23-4-2015 Total Marks: 70

Time: 11.00 a.m. to 2.00 p.m.

Instructions: 1) Q. 1 and 2 are compulsory.

- 2) Answer any three questions from Q. 3 to Q. 7.
- 3) All questions carry equal marks.
- 1. a) Select correct alternatives.

6

- 1) In VHDL, the mode of a port does not define
  - a) an input
  - b) an output
  - c) both an input and an output
  - d) the type of the bit
- 2) How are the statements between BEGIN and END not evaluated in VHDL?
  - a) Constantly
  - b) Simultaneously
  - c) Concurrently
  - d) Sequentially
- 3) The content of a simple Programmable Logic Device (PLD) consists of
  - a) fuse-link arrays
  - b) thousands of basic logic gates
  - c) advanced sequential logic functions
  - d) thousands of basic logic gates and advanced sequential logic functions
- 4) SPLDs, CPLDs and FPGAs are all which type of device?
  - a) PAL

b) PLD

c) EPROM

d) SRAM



- 5) The VHDL editor is
  - a) a graphics editor
  - b) a C program editor
  - c) a text editor
  - d) an I/O editor
- 6) The following VHDL ENTITY declaration code is incorrect because:

**ENTITY booly 2 IS** 

PORT (A, B, C, D, E: IN bit

X: OUT bit)

END booly2;

- a) missing semicolon
- b) missing "PORT END"
- c) mismatch in ENTITY name
- d) incorrect ENTITY name
- b) State true or false:

8

- 1) 10 bit ring counter has output frequency 10 Hz if input frequency is 100 Hz.
- 2) 22V10 PAL has 22 input and output pins.
- 3) MAX 7032 CPLD has 64 macro cells.
- 4) PAL has programmable AND plane and programmable OR plane.
- 5) Behavioural description in VHDL program includes process statement.
- 6) 4 bit binary adder is faster than carry look ahead adder.
- 7) Even parity checker are designed using XNOR gates.
- 8) A 4-bit full adder requires seven half adder.
- 2. Write a short note on:

14

- i) Synthesis
- ii) ALU
- iii) 3-bit ripple counter.



3. a) A sequential circuit with two D flip flops A and B, input X and output Y is specified by the following next state and output equations.

-3-

$$A(t+1) = AX + BX$$

$$B(t + 1) = A'X$$

$$Y = (A + B) X'$$

- i) Draw the logic diagram of the circuit
- ii) Derive the state table
- iii) Derive the state diagram.
- b) Explain carry look ahead adder.
- 4. a) Design 2 bit Gray Code Counter using JK flip flop.
  - b) Draw and explain block diagram of PLA.
- 5. a) Draw the structure of MAX 7000 macrocell. Explain in brief.
  - b) Write a VHDL code for SR flip flop.
- 6. a) Write a VHDL code for 4-bit binary adder.
  - b) Explain inertial and transport delay.
- 7. a) Implement the function using PAL.

$$f1 = \sum m (6, 3), \qquad f2 = \sum m (0, 4, 7)$$

b) Write a program for 1 bit full subtracter.

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#### M.Sc. (Part – I) (Semester – II) Examination, 2015 ELECTRONICS (Communication Science) (Old) Paper – VI: Microwave Engineering

Day and Date: Saturday, 18-4-2015

Time: 11.00 a.m. to 2.00 p.m.

Instructions: 1) Q. (1) and (2) are compulsory.

2) Answer any three questions from Q. 3 to Q. 7.

3) All questions carry equal marks.

1. Objective questions:

14

a) Select correct alternatives:

- 6
- 1) If only magnetic field is transverse to the direction of propagation and the electric field is not transverse is referred as

4) Use of scientific calculator is allowed.

- a) TM wave
- b) TE wave
- c) TEM wave
- d) HE wave
- 2) If  $E_z = 0$  and Hz = 0 then, it is indicated as
  - a) TE wave

b) TEM wave

c) TM wave

- d) None of the above
- 3) The insertion loss is contributed by
  - a) matched load resistance
  - b) matching between input and output load
  - c) mismatching loss at input
  - d) None of the above
- 4) For an open circuited line,  $z_L = \infty$ ,  $I_L = O$  and is given by
  - a) Zinsc = Zo tan h $\gamma l$
- b) Zinsc = Zo cot h $\gamma l$
- c) Zinsc =  $-iZo \cot \beta l$
- d) None of the above



		5)	The time taken by the electron to to the gap is called as	avel into the repeller space and back to	
			a) Repeller voltage	b) Transit time	
			c) Aperture effect	d) None of the above	
		6)	One of the following modes canno a) TE mode c) TEM mode	t exist in a rectangular waveguide b) TM mode d) None of the above	
	b)	St	ate <b>true</b> or <b>false</b> :		8
		1)	The knowledge of plane wave prop the propagation of microwaves.	pagation in very important to understand	
		2)	The electromagnetic wave inside a of patterns which are called as mo	waveguide can have an infinite number des.	
		3)	TWT is also used as broad band a	mplifiers in microwave applications.	
		4)	In TEM wave, only electric field is transvers	ansverse to the direction of propagation se.	
		5)	The short circuit termination produces ired point on microwave transm	uces an adjustable reactive load at the nission line.	
		6)	-	control the amount of microwave power er on a transmission line is called phase	
		7)	Gunn diodes are – Ve resistance of power oscillator at microwave free	devices which are normally used as low uencies.	
		8)	When microwave propagates with planes is called plane waves.	phase remaining constant over a set of	
2.	Wı	rite	short notes :		
	a)	W	rite note on wave propagation.		5
	b)	Ex	plain TWT.		5
	c)	Di	scuss Co-axial lines.		4



3.	a)	Starting from Maxwell's equation, derive the wave equations.	10
	b)	Explain the different types of wave polarization.	4
4.	a)	With the help of neat sketch, explain the operation of various types of dielectric bead supports used in coaxial connectors.	10
	b)	Explain Gunn effect.	4
5.	a)	Derive the equation for losses in co-axial lines.	10
	b)	What are strip lines ? Explain.	4
6.	a)	With the help of neat sketch, explain the different types of waveguide attenuators.	10
	b)	Discuss the stripline attenuators.	4
7.	a)	Derive the equation for attenuation in circular waveguide.	10
	b)	Explain rectangular waveguide with necessary equations.	4



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#### M.Sc. (Part – I) (Semester – II) Examination, 2015 ELECTRONICS (Communication Science) (Old) Paper – VII: Digital Design and VHDL Programming

i apei – vii .	Digital Design and	VIIDE I Togramming	
Day and Date : Tuesday, 21 Time : 11.00 a.m. to 2.00 p.		Total Marks :	70
2) Ar	(1) and Q. (2) are comp nswer any three questio Il questions carry equal	ons from Q. <b>(3)</b> to Q. <b>7</b> .	
1. Objective questions:			14
A) Select correct altern	native :		6
circuits ? 1) Full adder 4) JK flip flop		3	
<ul><li>2) Which of the fol Register?</li><li>1) Shift left</li><li>3) Parallel load</li></ul>	lowing capabilities are a  2) Shift right  4) Serial add  ct answer from the code	es given below :	
• • • • • • • • • • • • • • • • • • • •	of a JK flip flop is zero. It of Input J <sub>n</sub> and K <sub>n</sub> are respo b) X and d) X and	0	



5

	4)	A four bit modulo-16 ripple counter used JK flip flop. If progression delay of each FF is 50ms, then maximum clock frequency is equal to  a) 20 MHz  b) 5 MHz  c) 10 MHz  d) 4 MHz	
	5)	<ul> <li>Choose correct statement(s) from the following:</li> <li>a) PROM contains a programmable AND array and a fixed OR array</li> <li>b) PLA contains a fixed AND array and a programmable OR array</li> <li>c) PROM contains a fixed AND array and a programmable OR array</li> <li>d) PLA contains a programmable AND array and a programmable OR array</li> </ul>	
	6)	<ul> <li>Which of the following statements are correct?</li> <li>a) A flip flop is used to store 1 bit information</li> <li>b) Race around condition occurs in a JK flip flop when both the inputs are 1</li> <li>c) A transparent latch consists of a D type flip flop</li> <li>d) All of these</li> </ul>	
	B) Sta	ate <b>True</b> or <b>False</b> /Justify/ <b>One</b> line answer :	8
	1)	State diagram are used to represent sequential logic.	
	2)	Carry look ahead adder is slower than serial adder.	
	3)	EX-OR gates are useful as parity generators.	
	4)	Race around condition occurs in JK flip flop.	
	5)	MOD-6 ripple counter requires six flip flops.	
	6)	For implementation of combinational logic FPGA's are preferred.	
	7)	Entity includes signal and variable declaration.	
	8)	In VHDL code, process statement is used in architecture having behavioral description.	
2.	Attem	pt <b>all</b> :	
	1) Ex	olain 4-bit Johnson's counter.	5

2) Compare: Generics and Constants.

3) Write a short note on case statement.

3. A) Write a VHDL code for 4-bit comparator using 1-bit comparator as a component. 8 B) Write VHDL code for 1-bit latch. 4. A) Design 4-bit synchronous up counter using JK flip flop. 8 B) Write a short note on predefined attributes. 6 5. A) Define synthesis. Explain different types of synthesis. Write a synthesis code for 2:1 multiplexer. 8 B) Explain different types of delays. 6 6. A) Design 1 digit BCD adder. Differentiate binary adder and BCD adder. 8 B) Write a declaration for a function that test whether an integer is odd. The function declaration should appear in a package declaration. 6 7. A) Write down the truth tables, VHDL codes for ring counter using behavioral modeling. 8 B) Design a PLA to realize following three functions with internal connections: 6  $F_1(A, B, C, D, E) = A'.B'.D' + B'.C.D' + A'.B.C.D.E'$  $F_2(A, B, C, D, E) = A'.B.E + B'.C.D'.E$  $F_3(A, B, C, D, E) = A'.B'.D' + B'.C'.D'.E + A'.B.C.D$ 



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### M.Sc. (Part – I) (Semester – II) Examination, 2015 ELECTRONICS (Communication Science) (Paper – VIII)

#### **Microprocessors and Advanced Microcontrollers**

Day and Date: Thursday, 23-4-2015 Total Marks: 70

Time: 11.00 a.m. to 2.00 p.m.

Instructions: 1) Q. (1) and (2) are compulsory.

- 2) Answer any three questions from Q. 3 to Q. 7.
- 3) All questions carry equal marks.
- 1. a) Choose correct alternatives.

- 14
- 1) BHE of 8086 microprocessor signal is used to interface the
  - a) Even bank memory
- b) Odd bank memory

c) I/O

- d) DMA
- 2) In 8086 the overflow flag is set when
  - a) the sum is more than the 16 bits
  - b) signed number go out of range after the arithmetic operation
  - c) carry and sign flag are set
  - d) during subtraction
- 3) The least significant four bits of 16-bit number to be moved into a segment register should be
  - a) 0000

- b) 1111
- c) can be any four bit number
- d) none
- 4) The advantage of memory mapped I/O over I/O mapped I/O is
  - a) Faster
  - b) Many instructions supporting memory mapped I/O
  - c) Require a bigger address decoder
  - d) All of the above

2.



	5)	5) ANDLW k instruction may affect					
		a) C	b) DC	c)	Z	d) None	
	6)	PIR1 registe	r contains		_andPIE1 contain	ns bits.	
		a) Interrupt	Priority, Interrupt E	nabl	le		
		b) Interrupt	Flag, Interrupt Prio	rity			
		c) Interrupt	Priority, Interrupt F	lag			
		d) Interrupt I	Flag, Interrupt Enal	ole			
	7)	PIC 16F877 module.	consist of		_ channel and	bit ADC	
		a) 8,8	b) 10,8	c)	8, 10	d) none of these	
	8)	PSA (Presca	aler assignment) bi	t in t	he option register	equals to 1 then	
		a) Prescaler	renabled	b)	Prescaler disable	ed	
		c) Prescale	r assigned to WDT	d)	Prescaler assign	ed to TIMER0	
b)	Gi	ve <b>true/false</b>	statement.				6
	1)		p-codes are norma en and fetched from	•		de segment but can tra segment.	
	2)	The ASSUM is loaded and		he C	CS and DS register	s when the program	
	3)	The Trap flag	g controls the single	e ste	ep mode of operati	on of 8086.	
	4)	Not writable	bits in the status re	gist	er of PIC 16F877	are DC and C.	
	5)	Watch Dog 7	Timer avoids the ma	al fu	nctioning of the PI	C 16F877.	
	6)	PIC microco	ntroller RBO pin pri	mari	ily not used for exte	ernal interrupt input.	
Att	tem	pt <b>any three</b> .					14
a)	) Describe the function of the following pins and their use in 8086 based system i) NMI ii) HLDA.						
b)	Explain the different directives used in 8086 programming.						
c)	Explain the interrupt structure of PIC 16F877.						
d)	Explain how SPI bus can be used for I/O expansion.						



3.	a)	Explain in detail I2C module. Explain how to initialize MSSP module in I2C module.	8
	b)	Draw and explain the timing diagram for 8086 maximum mode memory write cycle.	6
4.	a)	Draw the functional block diagram of 8288. Explain the command and control signals of 8288.	8
	b)	Explain how relay can be interfaced with PIC. Write a program to toggle the relay with some delay.	6
5.	a)	Draw a power on reset circuitry and clock circuitry related to PIC family.	8
	b)	Interface 8255 to the 8086 in I/O mode.	6
6.	a)	Interface I/O processor 8089 to 8086 and explain how communication takes place between them.	8
	b)	With block diagram explain Watch Dog Timer.	6
7.	a)	What is an instructions pipelining in PIC? Why does a branch instruction introduces an extra cycle in the CPUs execution of instructions?	8
	b)	Explain the string instructions of 8086.	6



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#### M.Sc. (Part – II) (Semester – III) Examination, 2015 ELECTRONICS (Communication Science) Paper No. IX Communication System Design

	Paper No. IX Communication System Design					
Day and Date : Wednesday, 15-4-2015 Total Marks						
Time:	3.00 p.m. to 6.00	p.m.				
In	2) A	l. <b>(1)</b> and Q. <b>(2)</b> are nswer <b>any three</b> q <b>II</b> questions carry <b>c</b>	uestions from Q. (3)	to Q. <b>(7)</b> .		
1. A)	Select correct alt	ernative.		8		
1)	The cell site trans	smitter power incre	eased by 3dB. It mea	ns it is increased by		
	a) two	b) four	c) three	d) unchanged		
2)	Multipath fading i	is a type of	fading ?			
	a) large-scale	b) small – scale	c) both a and b	d) none of the above		
3)	Which of the follo	owing is digital mod	lulation technique?			
	a) ASK	b) FSK	c) PSK	d) All of the above		
4)	A loss of signal s	strength is called				
	a) Attenuation	b) Amplification	c) Fading	d) All of these		
5)	Which one of the	following is analog	g communication sys	tem?		
	a) AMPS	b) CDMA	c) GSM	d) GPRS		
6)	The technique to same rate is known		at the transmitter and	d receiver operate at		
	a) Synchronizati	on	b) Demodulation			
	c) Modulation		d) Scrambling			
7)	Redundancy of a	signal is truly rem	oved ins	ystem.		
	a) DPCM	b) PCM	c) Delta	d) PDM		
8)	The change in fre	equency is called				
	a) Doppler shift		b) Coherent time			
	c) Fading		d) Dispersion			
				P.T.O.		

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B) State true or false.

1) FHSS spectrum technique used in Bluetooth.

2) Durkins propagation model is indoor type of propagation model.



 $(6 \times 1 = 6)$ 

	3) The paging system usually employs FSK modulati	on technique.
	4) Functional architecture design is done after the pr	eliminary design review.
	5) Soft hand-off is called make before break hand-of	f.
	6) The propagation path loss increases with frequen	cy of transmission as well
	as the distance.	
2.	2. Attempt the following.	(14)
	1) Explain cordless telephone system.	5
	2) Explain in brief subsampling receiver.	5
	3) Write a short note on heat sink.	4
3.	3. a) Discuss the problems associated with homodyne rec	eiver. 8
	b) Explain concept of image reject receiver.	6
4.	4. a) Explain and derive the expression for link budget des model.	ign using path loss <b>10</b>
	b) Explain channel assignment strategies in cellular sys	tem. 4
5.	5. a) Explain cellular telephone system in detail with suitab	ele diagram. 8
	b) Explain Hand-off mechanism with its types.	6
6.	6. a) Explain FHSS with slow hopping and fast hopping.	8
	b) Explain packaging concept in system design conside	ration. 6
7.	7. a) With suitable diagrams explain working of PSTN.	8
	b) Explain the architecture of Digital-IF receivers.	6

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# M.Sc. (Part - II) (Semester - III) Examination, 2015

Satellite Communi		,
Day and Date: Friday, 17-4-2015		Total Marks: 70
Time: 3.00 p.m. to 6.00 p.m.		
Instructions: 1) Q. 1 and 2 are c 2) Answer any thr 3) All questions ca	r <b>ee</b> questions from	Q. <b>3</b> to Q. <b>7.</b>
1. Objective questions:		8
A) Select correct alternatives :		
1) The maximum distance between	n earth satellite in a	n elliptical orbit is
a) Posigrade	b) Retrograde	
c) Perigee	d) Apogee	
2) Which of the forces act on satel	lite in an orbit ?	
a) Centrifugal b) Centripetal	c) Both	d) None
3) Transponder converts	_frequency to	frequency.
a) Uplink, downlink	b) Downlink, upli	nk
c) Both (a) & (b)	d) None of these	
4) Tracking system is used		
a) To collect data from different station	sensors and send	s to the control earth
<ul><li>b) For the determination of the craft</li></ul>	current orbit and p	position of the space
c) For making changes in altitud	de and orbit correct	ion
d) For controlling communication	on system	



	5)	In which of the f satellite to move		t of satellite s	subsystem make a help to	
		a) Frequencies	translator			
		b) Attitude conti	rol subsystem			
		c) Propulsion su	-			
		d) Communicat	ion subsystem			
<ol> <li>In which of the following refers to satellite service that can be recei- many unspecified location by relatively simple receiver only earth stat</li> </ol>						
		a) FSS	b) BSS	c) MSS	d) None	
	7)	The satellite sys	tem which provid	des global co	mmunication service is	
		a) Intelsat	b) Iridium	c) Eulelsat	d) Both a) and b)	
	8)	For C band the f	requency range			
		a) 200-400 Mhz		b) 3400-64		
		c) 50-100 Mhz		d) 10.95-14	4.5 Mhz	
	B) Sta	ate <b>true</b> or <b>false</b>	:			6
<ol> <li>In circular orbit speed of satellite is not constant.</li> <li>During an eclipse, the satellite is powered by earth station.</li> </ol>			ant.			
			earth station.			
	3) LEO is placed in the range 400 to 1000 mi above the earth.			ove the earth.		
	4) Attitude correction is made by firing jet thrusters.				ers.	
5) Military satellites often operate in the C band.						
	6)	Single conversion signal to the down	•	ses a single r	nixer to translate the uplink	
2.	Write a short note:					
	a) DBS	S-TV receiver				5
	b) TDN	ИΑ				5
	c) Lau	nches and launcl	n vehicles.			4
3.	a) Brie	efly describe Attit	ude and Orbit Co	ontrol System	n (AOCS).	8
	b) Witl	n the help of bloc	k diagram explai	n the working	g of transponder.	6



4.	a)	A 12 GHz receiver consists of an RF stage with $G_1 = 30$ dB and noise	
		temperature $T_1 = 20K$ , a down converter with gain $G_2 = 10$ dB and noise	
		temperature $T_2 = 360$ K and an IF amplifier stage with $G_3 = 15$ dB and noise	
		temperature $T_3 = 1000$ K. Calculate the effective noise temperature, individual	
		noise figure of the three stages and overall noise figure of the system. Take	
		reference temperature to be 290 K.	8
	b)	Explain the terms angle of inclination and angle of elevation with neat diagram.	6
5.	a)	What are Kepler's three laws of planetary motion? Explain the parameter to	
		describe the satellite orbit.	10
	b)	What is meant by geostationary satellite?	4
6.	a)	Explain the term :	8
		i) C-band and Ku band	
		ii) delay and throughput considerations.	
	b)	Explain Radio and Satellite Navigation.	6
7.	a)	Explain in detail 'Teledesic'.	8
	b)	Explain the working of VSA T hub master control station.	6

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# M.Sc. II (Semester – III) Examination, 2015 ELECTRONICS (Communication Science) Paper – XI: Digital Communication

		•	munication Sci Il Communication	•	
Day and Date : I Time : 3.00 p.m	_			Max. Marks : 7	0
Instr	2)	_	re <b>compulsory</b> . <b>ree</b> questions from arry <b>equal</b> marks.	n Q. <b>3</b> to Q. <b>7</b> .	
1. a) Select c	orrect alterna	atives:			6
1) Gran a) P		associated with b) DM	c) DPCM	d) QAM	
	$\leftrightarrow$ 0 as $\leftrightarrow$ 1	b) $pk \rightarrow 1/4$	c) $pk \rightarrow 0$	d) pk $\rightarrow$ 1/2	
a PC		ing the same bar		n overall S/N ratio of d) none of these	
a) le	_	age	nvolution code is k b) length of modu d) constraint leng	ılo two adders	
•	ents of the rig	nvolutional enc htmost b) K – 1	_stages.	represented by the d) $N-2$	
a) Tı	nore practica ree structure olynomial rep		ximum likelihood d b) State represer d) Trellis structur	ntation	

#### b) State True or False:

8

- 1) A random process defined are ergodic if of all types of ensemble average are not changeable.
- 2) PCM systems use non uniform quantization in order to raise SNR for low level signals.
- 3) Parity check code can correct and detect two errors.
- 4) Hartley law states that amount of information is proportional to the bandwidth of the channel.
- 5) According to Shannon's theorem the output from any source of rate R can be coded and transmitted over a channel of capacity C with the condition that C<R.
- 6) A carrier recovery is not required in QPSK.
- 7) The slope overload problem of ADM can be overcome using DM.
- 8) Mth power loop technique is used for carrier recovery.

#### 2. Write short notes:

a) Properties of convolution codes.

5

b) Uniform and Non uniform Quantization.

5

c) Normal PDF.

4

3. a) Explain the properties of Entropy. An event has six possible outcomes with the probabilities 1/2, 1/4, 1/8, 1/16, 1/32 and 1/32 respectively. Find the entropy of the system. Also find the rate of information if there are 16 outcomes per second.

8

b) Explain characteristics of Burst error and Random error correcting codes.

6

4. a) The generator polynomial for a (15, 7) cyclic code is  $q(x) = 1 + x^4 + x^6 + x^7 + x^8$ .

8

- i) Find code vector in systematic form for the message polynomial  $D(x) = x + x^2 + x^5$
- ii) Find code vector in non systematic form for the message polynomial  $D(x) = x + x^2 + x^3$
- b) What is the importance of symbol synchronization? Explain any one method of symbol synchronization.

5. a) Find out all code words for a(6, 3) linear block code with given Generator matrix.

8

$$G = \begin{pmatrix} 100 & 110 \\ 010 & 011 \\ 001 & 101 \end{pmatrix}$$

Also find its parity check matrix. If the received code vector is 1001011, is the correct code vector?

b) With block diagram explain working of ADM.

6

6. a) Apply the Shannon-Fano coding procedure for the following message ensemble. Calculate its efficiency.

8

$$[X] = [x_1 \quad x_2 \quad x_3 \quad x_4 \quad x_5 \quad x_6 \quad x_7 \quad x_8]$$

 $[P] = [1/8 \quad 1/16 \quad 1/8 \quad 1/16 \quad 1/16 \quad 1/4 \quad 1/16 \quad 1/4]$ 

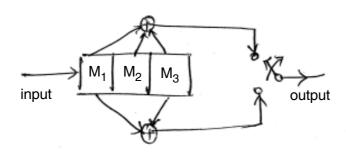
Take M = 2

b) Compare digital modulation schemes with respect to Bandwidth, power requirements and Equipment complexity.

6

7. a) Find convolution code for the message 11011 for given convolution encoder. (Fig. 1) Also draw code tree for the same.

8



b) What is polynomial representation? Explain the same with suitable example.

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# M.Sc. – II (Semester – III) Examination, 2015 ELECTRONICS (Communication Science) Paper – XII: Internetworking and Data Communication

Day and Date: Wednesday, 22-4-2015 Total. Marks: 70

Time: 3.00 p.m. to 6.00 p.m.

Instructions: 1) Q. 1 and Q. 2 are compulsory.

- 2) Answer any three questions from Q. 3 to Q. 7.
- 3) All questions carry equal marks.
- 1. Objective questions.

14

a) Choose the correct alternatives:

8

- 1) Routers operate at which layer of the OSI Model?
  - A) Network
  - B) Physical
  - C) MAC sublayer of the Data Link layer
  - D) Transport
- 2) Bits are packaged into frames at what layer of the OSI Model?
  - A) Physical

B) Application

C) Presentation

- D) Data Link
- 3) Which of the following are benefits of using a layered network model?
  - A) It facilitates troubleshooting
  - B) It breaks the comples process of networking into more managable chuncks
  - C) It allows layers developed by different vendors to interoperate
  - D) All of the above



4) The layer of the OSI Model, from the top down are			
	A)	Physical, Data Link, Network Application	k, Transport, Session, Presentation,
	B)	Application, Presentation, Se Physical	ession, Transport, Network, Data Link,
	C)	Application, Encryption, Network Physical	work, Transport, Logical Link Control,
	D)	Session, Presentation, Data 1	ransport, MAC, Network, Physical
5)	WI	nich of the following operates a	at the Presentation layer?
	A)	UDP	B) FTP and HTTP
	C)	MIDI and JPEG	D) SMTP
	E)	All of the above	
6)	WI	nich of the following are Trans	oort layer protocols ?
	A)	CISC	B) TCP and UDP
	C)	HTTP and FTP	D) ATM
7)	WI	nich of the following are consid	lered to be the upper layer protocols?
	A)	Application, Presentation and	Session
	B)	Application	
	C)	Application, Presentation, Ses	ssion and Transport
	D)	Application and Presentation	
8)	Flo	ow control takes place at what	layer?
	A)	Transport	B) Application
	C)	Data Link	D) Network



	b) State <b>True</b> or <b>False</b> :	6
	1) Bridge and Routers work on same principle.	
	2) Terrestrial Microwave is an Unguided media.	
	3) Routing can be used for mobile hosts.	
	4) Congestion control can be done in virtual subnets and Choke packets	<b>3.</b>
	5) Domain Name Specification is Website address.	
	6) Sliding window is a Data Link Protocol.	
2.	Answer in brief	14
	1) List the modes of operation of HDLC.	5
	2) What is the 'Channel Allocation Problem'?	5
	3) Compare between LAN and WAN.	4
3.	a) State the functions of each layer in an OSI model.	10
	b) Compare between TCP/IP and OSI model.	4
4.	a) What is congestion in networks? How is it controlled in TCP?	10
	b) Define DNS and give examples.	4
5.	a) Explain the Shortest Path Routing Algorithm.	10
	b) Compare between flow based and mobile host routing.	4
6.	a) State and elaborate on the Design issues of physical layer.	8
	b) What is unguided media? How is it dealt with?	6
7.	a) With block schematics explain the different types of modems.	8
	b) Why do we need Computer Networks? State the applications of Comput Networks.	er 6



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## M.Sc. (Part – II) (Semester – IV) (Electronics) (Communication Science) Examination, 2015

	Paper – )	(III : BROAD	BAND COMMU	JNICATIO	N
-	ate : Thursday, 1 0 p.m. to 6.00 p.r				Total Marks: 70
Ins	,	nswer <b>any thre</b>	e compulsory. ee questions from rry equal marks.	n Q. <b>3</b> to Q. 7	7.
1. A) Ch	oose correct alte	rnatives.			8
			, a preplanned ro	ute is estab	lished before
	any packets are	sent.			
	a) ATM		b) Virtual circuit		
	c) Datagram		d) None of these	9	
2)	The internationa	I ISDN number	is maximum of $\_$	(	digits.
	a) 10	b) 20	c) 15	d) 25	
•	•		e the unidirection ue identifier valu	•	of ATM cells
	a) Virtual channe	el link	b) Virtual path		
	c) Virtual channe	el	d) Virtual path li	nk	
•			l duplex 64 Kbps channel.		channels
	a) B, C	b) B, D	c) B, H	d) D, H	
5) The SONET'slat the approrpriate signaling sp			-	e for end trar	nsport of data
	a) Photonic	b) Section	c) Line	d) Path	
6)	I. 400 series star	ndard option			
	a) General struc	ture	b) Service capa	bilities	
	c) Maintenance	principles	d) User-network	interfaces	

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	/	) ISDN relies on _	Kbp	os connections.		
		a) 16	b) 32	c) 64	d) 128	
	8	) ISDN datalink lay	yer uses	protocol		
		a) LAPD	b) LAPB	c) LAPF	d) HDLC	
	B) C	hoose correct opti	on <b>True/False</b> .			6
	a	) GFC function go header.	enerates flow	control informati	on for placement in	cell
	b	) Retrieval service	es of B-ISDN a	re distributive se	rvices.	
	С	) I.113 standard d	efines the voca	abulary terms for	B-ISDN.	
	d	) DLCI is used to i	dentify logical o	connection.		
	е	) Reference point	S separates tw	o networks in IS	DN.	
	f	) Primary access i	is intended to p	rovide low speed	d capabilities.	
2.	Ansv	wer <b>any three</b> :				(5+5+4)
	1) E	Explain the terms -	- circuit switchii	ng and packet sv	vitching.	
	2) [	Draw ISDN protoco	ol at user netwo	ork interface.		
	-	Explain central que	_	vitching.		
	4) L	Draw LAPB and LA	PF protocols.			
3.	a) E	xplain in detail ATI	M switching ma	atrix.		10
	b) L	ist the I series star	ndards for ISDI	٧.		4
4.	a) E	xplain in detail the	header format	for ATM cell.		8
	b) E	xplain AAL-5 in de	tail.			6
5	a) F	xplain header erro	r control in ATN	Л		8
J.	,	xplain neader end xplain SONET/SD		vi.		6
	•	•				· ·
6.	-	xplain physical lay				8
	b) E	xplain ATM cell pr	ocessing in a s	witch.		6
7.	a) E	xplain frame mode	e call control alt	ernatives.		8
	b) C	compare X.25 and f	frame relay.			6



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### M.Sc. II (Semester – IV) Examination, 2015 ELECTRONICS (Communication Science) Paper – XIV: Mobile Communication

		•	imunication Scientification in S	•
Day ar	nd Date : Saturday	, 18-4-2015		Max. Marks: 70
Time :	3.00 p.m. to 6.00	p.m.		
In	2) A 3) <b>A</b>	D. <b>1</b> and Q. <b>2</b> are <b>co</b> nswer <b>any three</b> qu I <b>II</b> questions carry <b>e</b> I <b>se</b> of scientific cald	uestions from Q. <b>3</b> to <b>qual</b> marks.	o Q. <b>7</b> .
1. Obje	ective questions.			
A) C	hoose the correct	alternatives.		8
1)	Bluetooth is a wismall area.	ireless	technology that co	nnects devices in a
	a) LAN	b) MAN	c) PAN	d) WAN
2)	is bas	ed on client / serve	r model.	
	a) DHCP	b) FTP	c) HTTP	d) HDLC
3)	A smaller cluster	size in cellular syst	em leads to a	capacity.
	a) Small	b) Large	c) Medium	d) All of these
4)	TCP d	oes not maintain en	d-to-end semantics.	
	a) Snooping	b) Indirect	c) Mobile	d) Traditional
5) is a second generation cellular phone system based on CDMA and DSSS.				pased on CDMA and
	a) GSM	b) D-AMPS	c) IS-95	d) GPRS
6)	During handover connection qualit	<del>-</del>	rerouting all connect	ions and maintaining
	a) WATM	b) WLAN	c) WMAN	d) WWAN

**SLR-JP-225** 7) HIPERLAN is defined by d) Both (a) and (b) b) ETSI c) ISO a) IEEE 8) Wireless LAN standards defined by committee. a) IEEE 802.11 b) IEEE 802.15 c) IEEE 802.16 d) IEEE 802.5 B) State the following statement is **True** or **False**. 6 1) TDMA is a discontinuous transmission scheme. 2) IMSI is a unique identify number, resides in EIR. 3) The forward link in CDMA IS – 95 system uses 63 pilot channels. 4) The access point is essential in ad hoc network. 5) Bluetooth is an example of IEEE 802.11. 6) The cluster size reserved for GSM is 7. 2. Answer in brief. a) Mention in brief application of wireless communication. 5 b) Compare: Infrared and Radio transmission. 5 c) Explain in brief Snooping TCP. 4 3. a) Explain IEEE 802.11 system architecture and protocol architecture in detail. 10 4 b) Explain advantages and disadvantages of WLAN. 4. a) Draw GSM network architecture. Explain the subsystems of GSM network with its functionality. 8 b) Write a short note on mobile and wireless devices. 6 5. a) What is mean by cell? How the cellular concept helped to solve the problem of spectral congestion and user capacity? 8 b) Write a note on WATM services. 6 10 6. a) Explain mobile IP in detail with IP packet delivery mechanism. b) Compare: Infrastructure and ad-hoc network. 4 7. a) Explain the following term with respect to Traditional TCP 8 i) Congestion control ii) Slow start iii) Fast retransmit /fast recovery. b) Explain registration mechanism in mobile IP. 6



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### M.Sc. (Part – II) (Semester – IV) Examination, 2015 ELECTRONICS (Communication Science) Paper – XV: Fiber Optic Communication

		XV : Fiber Opti			n on	
Day and Date : Tuesday, 21-4-2015  Time : 3.00 p.m.to 6.00 p.m.				Max. Marks	70	
	2) Que	uestions carry <b>equ</b> stion <b>1</b> and <b>2</b> are <b>co</b> e <b>any three</b> questio	тр	ulsory.	lo. <b>3</b> to <b>7.</b>	
1. Answ	er the following :					14
A) Ob	jective questions :					8
·	of Radiation".  a) Stimulated Em  c) Absorption		b) d)	Spontaneous I		
2)	a) Fabri-parrot ca	onant cavity used is vitv		Velocity modu	lation cavity	
	c) Mirror cavity	,	,	None	ianon ourny	
3)	into the upper er	e population invers nergy level <i>E2 an</i> process is achieve as	d h	ence obtain a	non-equilibrium	
	a) Bombing	b) Pumping	c)	Exiting	d) Inversing	
4)	Attenuation in fibe	r is calculated as				
	a) 10 log (Pi/Po)		b)	10 log (Po/Pi)		
	c) 20 log (Pi/Po)		d)	20 log (Po/Pi)		
	Where Pi-input	power, Po-output p	owe	er.		

2.

	5) Extrinsic absorption is caused due to			
		a) Water molecules in fiber	b) Metallic ions in fiber	
		c) Bubbles in fiber	d) Both a) and b)	
	6)	LED issource.		
		a) Coherent	b) Incoherent	
		c) Directional	d) Unidirectional	
	7)	Simple connectors are	_technique for fiber connection.	
		a) Semi permanent	b) Permanent	
		c) Removable	d) None of these	
	8)	If light is incident on air-core bounda light is remains inside the core and is		
		a) Acceptance angle	b) Critical angle	
		c) N. A.	d) Refraction angle	
B)	Tr	ue or false:		6
	1)	LED has larger line-width than laser.		
	2) In optical fiber communication, the attenuation is usually expressed in decibels per unit length (i.e. $dB \ km - 1$ ).			
	3)	The alignment losses are due to fiber re	ough ends, fiber placement in cable.	
	4)	Figure below shows the internal alignr	ment loss.	
	5)	The disadvantage of optical fiber is th	at groper's eats optical fiber.	
	6)	The optical carrier frequency is in the	range of 103 to 106 Hz.	
An	sw	er the following :		14
a)	Wı	rite a note on -Use of optical fiber in civ	vil applications.	5
b)	) Differentiate between optical fiber communication system and general communication system.			
c)	Wı	rite a note on population inversion.		4



3.	Answer the following questions:	14
	a) Explain Ray theory of transmission for optical fiber. Also define critical angle, acceptance angle.	10
	b) Explain different modes in optical fiber.	4
4.	Answer the following questions:	14
	a) Explain liquid phase optical fiber formation technique.	8
	b) Explain dispersion in optical fiber.	6
5.	Answer the following questions:	14
	a) Explain Fusion splicing of optical fiber.	8
	b) Explain hetero junction laser with neat diagram.	6
6.	Answer the following questions:	14
	a) Explain principle of LED in details.	8
	b) Explain Avalanche photodiode.	6
7.	Answer the following questions:	14
	a) When the mean optical power launched into an 8 km length of fiber is 120 $\mu$ W, the mean optical power at the fiber output is 3 $\mu$ W. Determine :	
	<ul> <li>a) the overall signal attenuation or loss in decibels through the fiber assuming there are no connectors or splices.</li> </ul>	
	b) the signal attenuation per kilometer for the fiber.	
	<ul> <li>c) the overall signal attenuation for a 10 km optical link using the same fiber with splices at 1km intervals, each giving an attenuation of 1 dB</li> </ul>	
	d) the numerical input/output power ratio in (c).	8
	b) Explain coupling of optical fiber with LED.	6

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### M.Sc. (Part – II) (Semester – IV) Examination, 2015 ELECTRONICS (Communication Science) Paper No. – XVI: Communication Protocols

Day and Date : Thursday, 23-4-2015 Total Marks : 70

Time: 3.00 p.m. to 6.00 p.m.

Instructions: 1) Q. No. 1 and 2 are compulsory.

- 2) Answer any three questions from Q. No. 3 to Q. No. 7.
- 3) All questions carry equal marks.
- 1. A) Multiple choice questions:

6

- 1) Flex Ray protocol is used in
  - a) Optoelectronics

b) Microcontroller

c) Automobiles

- d) All applications
- 2) Which of the following is not a 1G standard?
  - a) Nippon telephone and Telegraph
  - b) Global system for mobile communication
  - c) Advanced Mobile system for Phone System
  - d) Nordic Mobile Telephones
- 3) In Signaling System 7 of GSM, STP stands for
  - a) Standalone Port

- b) Signal Transfer Port
- c) Service Transfer port
- d) None of the above
- 4) Modulation technique of IEEE 802.11b is
  - a) FSK
- b) PPM
- c) QAM
- d) PSK
- 5) In GSM signaling protocol network Layer-3 is divided into
  - a) 2-Sublayers

b) 4-Sublayers

c) 3-Sublayers

- d) 5-Sublayers
- 6) In UTRAN mobility managed by
  - a) SRNS

b) CRNC

c) DRNC

d) None of the above

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	B)	State true and false:	8
		1) Cells are always hexagonal.	
		2) MIP maintains active TCP connections and UDP port binding.	
		3) GPRS roaming is more difficult than GSM.	
		4) RS-422 supports double ended mode operation.	
		5) In 3G data rate of fast moving users is up to 2MBPS.	
		6) FCCH is broadcast logical channel of GSM.	
		7) I <sup>2</sup> C is faster than USB.	
		8) Use of Wi-Fi is simple for local area network.	
2.	A)	Explain GPRS logical channel.	5
	B)	Compare between MIPv6 and MIPv4.	4
	C)	Specify limitations of 1G cellular network.	5
3.	A)	Explain propagation models for wireless networks. Derive the expression for Two-Ray Ground reflection model.	10
	B)	Discuss UMTS interfaces.	4
4.	A)	Explain briefly 2G wireless cellular networks. Specify various standards of	
		2G networks.	10
	B)	Explain the term "MAGIC" related to 4G IP-network.	4
5.	A)	Draw and explain GSM network architecture.	10
	B)	Write a note on SGSN of GPRS.	4
6.	A)	Explain with suitable block diagram of the architecture of 802.16 standards.	10
		Explain the LIN protocol briefly.	4
7.	A)	List out different radio access technologies utilized under IMT-2000 group of	
	,	3G network.	10
	B)	What is I <sup>2</sup> C protocol ? List benefits of I <sup>2</sup> C.	4

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